

Post-silicon validation and debug

Deskripsi Lengkap: <https://lib.ui.ac.id/detail?id=9999920521391&lokasi=lokal>

Abstrak

This book provides a comprehensive coverage of System-on-Chip (SoC) post-silicon validation and debug challenges and state-of-the-art solutions with contributions from SoC designers, academic researchers as well as SoC verification experts. The readers will get a clear understanding of the existing debug infrastructure and how they can be effectively utilized to verify and debug SoCs.