

Low power RF circuit design in standard CMOS technology

Alvarado, Unai, author

Deskripsi Lengkap: <https://lib.ui.ac.id/detail?id=20418147&lokasi=lokal>

Abstrak

This book presents the basic techniques available to design low power RF CMOS analogue circuits. It gives circuit designers a complete guide of alternatives to optimize power consumption and explains the application of these rules in the most common RF building blocks, LNA, mixers and PLLs. It is set out using practical examples and offers a unique perspective as it targets designers working within the standard CMOS process and all the limitations inherent in these technologies.