

## Hardware/software co-design for heterogeneous multi-core platforms: the hArtes Toolchain

Deskripsi Lengkap: <https://lib.ui.ac.id/detail?id=20398342&lokasi=lokal>

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### Abstrak

HW/SW co-design for heterogeneous multi-core platforms describes the results and outcome of the FP6 project which focuses on the development of an integrated tool chain targeting a heterogeneous multi-core platform comprising of a general purpose processor (ARM or powerPC), a DSP (the diopsis) and an FPGA. The tool chain takes existing source code and proposes transformations and mappings such that legacy code can easily be ported to a modern, multi-core platform.